

09/740,174

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,767,784 B2
DATED : July 27, 2004
INVENTOR(S) : Porter et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 2,

Line 2, "bipolar transistor and an" should read -- bipolar transistor 20 and an --;

Column 11,

Line 58, "first voltage input V_{CC} " should read -- first voltage input V_{CC} , --;

Column 12,

Line 42, "layer first" should read -- layer/first --;

Column 13,

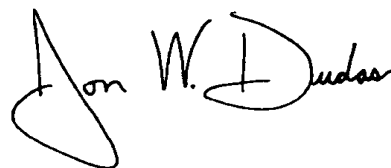
Line 67, "conductivity," should read -- conductivity; --; and

Column 14,

Line 25, "fanning" should read -- forming --.

Signed and Sealed this

Eighteenth Day of January, 2005

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D" at the end.

JON W. DUDAS
Director of the United States Patent and Trademark Office